

Pre thesis submission seminar by the seven Research Scholars was conducted on 2<sup>nd</sup> May, 2016 at 10:00 am onwards at the Seminar Hall of NIT Arunachal Pradesh.

| Sl No. | Name of the Scholar           | Title of the thesis   |
|--------|-------------------------------|---|
| 1      | Shri Koj Sambyo, CSE          | Study and analysis of reducing delay in cloud computing architecture  |
| 2      | Shri Subhasish Banerjee, CSE  | Study and Development of Various Schemes to Achieve the Perfect Security over Insecure Communication Networks                                 |
| 3      | Shri Sahadev Roy              | Development of Efficient Minimization Technique for Combinational / Sequential Logic Systems  |
| 4      | Shri Manash Pratim Dutta, CSE | An approach to develop protocols for Automatic Variable Key (AVK) to get high security and wide flexibility in cryptosystem                   |
| 5      | Yang Saring, ECE              | Investigation on new & modified Packet Combining and Aggressive Packet Combining Schemes for Reliable Error Correction with Higher Throughput |
| 6      | Yaka Bulo, ECE                | Studies on Various schemes of Aggressive Packet Combining and Packet Combining Scheme to Achieve Higher Reliability and Throughput            |
| 7      | Shri Anish Kumar Saha, CSE    | Green Computer Network Architecture: Analysis and Design  |